TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

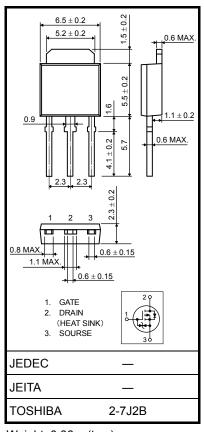
2SJ681

Relay Drive, DC–DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON-resistance: $R_{DS (ON)} = 0.12 \Omega$ (typ.)
- High forward transfer admittance: |Y_{fs}| = 5.0 S (typ.)
- Low leakage current: $I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -60 \ V)$
- Enhancement mode: $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-60	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	-60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	-5	А	
	Pulse(Note 1)	I _{DP}	-20	А	
Drain power dissipation	1	PD	20	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	40.5	mJ	
Avalanche current		I _{AR}	-5	А	
Repetitive avalenche e	nergy (Note 3)	E _{AR}	2	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -25 V, T_{ch} = 25°C (initial), L = 2.2 mH, R_G = 25 Ω , I_{AR} = -5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

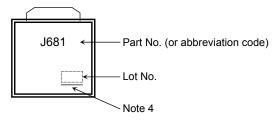
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	_		±10	μA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	_	_	-100	μA
Drain-source breakdown voltage		V (BR) DSS	I _D = -10 mA, V _{GS} = 0 V	-60	_	_	V
		V (BR) DSX	I _D = -10 mA, V _{GS} = 20 V	-35	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = -10 V, I _D = -1 mA	-0.8	_	-2.0	V
Drain-source ON resistance		Pro (o) II	V _{GS} = -4 V, I _D = -2.5 A	_	0.16	0.25	0
		R _{DS (ON)}	V _{GS} = -10 V, I _D = -2.5 A	_	0.12	0.17	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = -10 V, I _D = -2.5 A	2.5	5.0	_	S
Input capacitance		C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	700	_	pF
Reverse transfer capacitance		C _{rss}			60	_	
Output capacitance		Coss			90	_	
Switching time	Rise time	tr	V_{GS} -10 V C_{GS} -10 V C_{GS} $R_L =$ 12Ω VDD \approx -30 V	_	14	_	
	Turn-on time	t _{on}		_	24	_	ns
	Fall time	t _f		_	14	_	
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s		95	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	15	_	nC
Gate-source charge		Q _{gs}	V _{DD} ≈ −48 V, V _{GS} = −10 V, I _D = −5 A	_	11	_	
Gate-drain ("miller") charge		Q _{gd}] [4	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	-5	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	-20	А
Forward voltage (diode)	V _{DSF}	I _{DR} = -5 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -5 A, V _{GS} = 0 V		40		ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 50 A / μs	_	32	_	nC

Marking

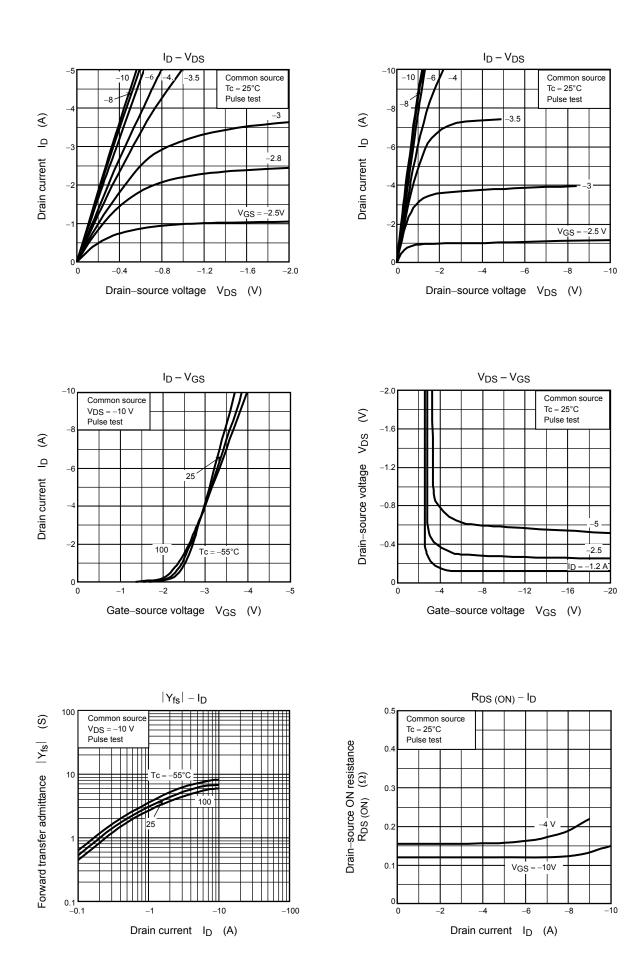


Note 4: A line under a Lot No. identifies the indication of product Labels.

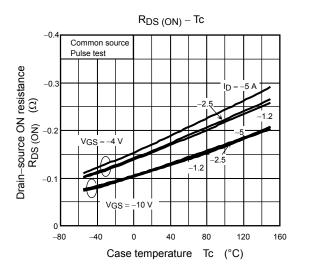
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

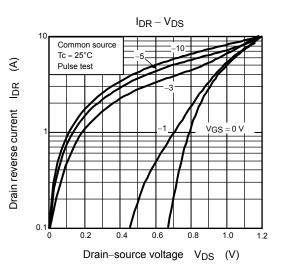
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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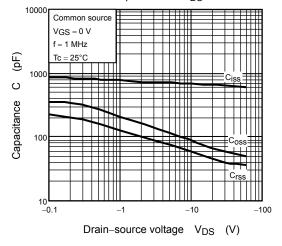


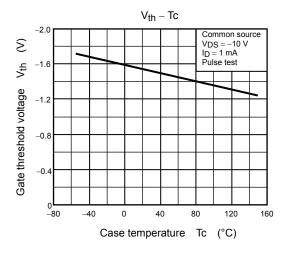
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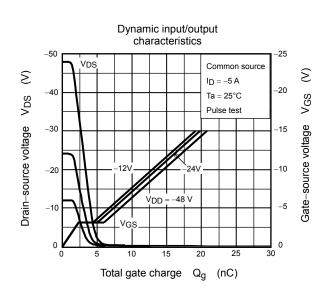


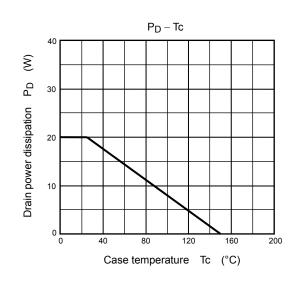


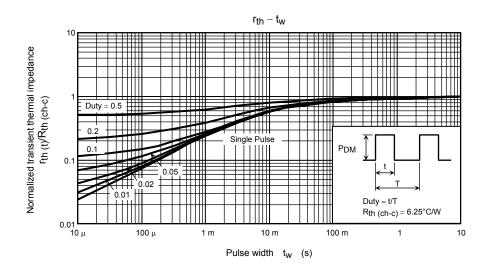
Capacitance – V_{DS}

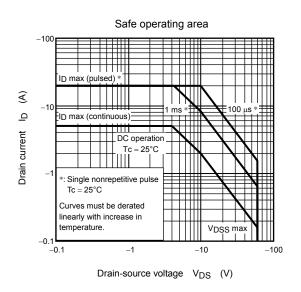


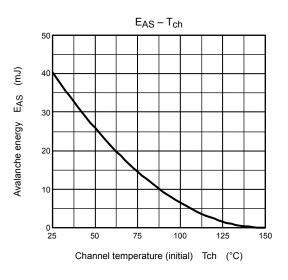


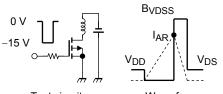






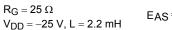








Wave form $E_{AS} = \frac{1}{2} \cdot L \cdot l^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$



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